

EE126 Lab Report Guidelines

Please follow this outline when writing your report.

1. Cover Page

Must Include:

- lab title and number
- course number
- lab section
- your name
- TA name
- due date

2. Introduction

State the purpose of the laboratory and give a brief overview of the goals which you expected to achieve and how you did or did not achieve them.

3. Procedure

Go into details over the steps you took to complete the laboratory. Also go over any equipment (hardware and software) which you used.

4. Results

Go over the tests which you performed to verify that your circuits function correctly. Also determine the delay in the circuits using the timing waveforms. Include the following waveforms:

5. Conclusion

Discuss how successful you were in meeting your goals stated in the introduction. Also discuss what you have learned and what you could have done to get better results if anything.

6. Appendix

- Block Level Schematic of the ALU with a description of all the inputs and outputs
- All Verilog & VHDL code