

RUIDA YUN

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HIGHLIGHTS OF QUALIFICATIONS

- ◆ Proven success handling electrical engineering research projects from inception through completion.
- ◆ High level of technical expertise utilizing scientific and engineering software including Matlab/Octave, C/C++, Cadence, OrCad, ADS2004, Synopsis, ModelSim and Microwind.
- ◆ Network Analyzer, Spectrum Analyzer

TECHNICAL PROJECTS

- ◆ Booth Encoded Wallace tree Multiplier, 2006
Cadence extensively used, from concept to layout, www.eecs.tufts.edu/~ryun01/vlsi
- ◆ Masters Thesis: Calibrating pipeline AD-converter design and implementation, 2005
- ◆ One 1.5bit Stage of a Pipelined ADC, 2005
- ◆ Design and Layout 3-bit Comparator with Low-voltage Supply, 2005
- ◆ Implementation of arithmetic transform algorithm based on CUDD, 2005
- ◆ FIR-Filter Design with VHDL, 2004
- ◆ Phase Detectors Design, 2004
- ◆ Development of Controller of Low-voltage Reactive Power Compensator, 2002 – 2003
- ◆ Bachelors Thesis: An algorithm for reactive power optimization of the distribution network, 2003

EDUCATION&CREDENTIALS

Tufts University, US
PHD STUDENT, ELECTRICAL ENGINEERING, Pursuing

Royal Institute of Technology (KTH), Stockholm, Sweden
MASTER OF SCIENCE, SYSTEM-ON-CHIP DESIGN, 2006

Zhejiang University, Hangzhou, China
BACHELOR OF ENGINEERING, ELECTRICAL ENGINEERING & AUTOMATION, 2003

AWARDS

- ◆ Outstanding Graduate Student with honor – Zhejiang University, 2003
- ◆ Outstanding Degree Thesis – Zhejiang University, 2003
- ◆ Entered the graduate program with an admission test waiver, 2002
- ◆ Third-class Scholarship for Excellent Students, 2002
- ◆ Second-class Scholarship for Excellent Students, 2001
- ◆ Outstanding Student Leader, 2001